



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: L. James Hwang et al.
Assignee: Xilinx, Inc.
Title: Method and System for Resource Allocation in FPGA-Based System-On-Chip (SoC)
Serial No.: 10/082,441 File Date: 2-22-02
Examiner: Unknown Art Unit: 2151
Docket No.: X-1000 US Conf. No.: 6299

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This Supplemental Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

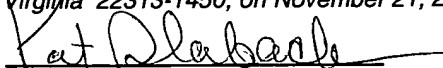
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Respectfully submitted, .


Kim Kanzaki
Attorney for Applicants
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PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO

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Complete if Known

Application / Conf. No.	10/082,441 / 6299
Filing Date	February 22, 2002
First Named Inventor	L James Hwang
Art Unit	2151
Examiner Name	Unknown
Attorney Docket Number	X-1000 US

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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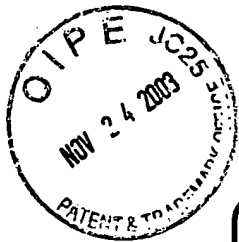
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¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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2151

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	Filing Date	February 22, 2002
	First Named Inventor	L. James Hwang
	Examiner Name	Unknown
	Art Unit	2151
	Patent No.	NOV 28 2003
Mail Stop: NON-FEE AMENDMENT	Attorney Docket Number	X-1000 Technology Center 2100
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Attn: Kim Kanzaki		
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Date	November 21, 2003	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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